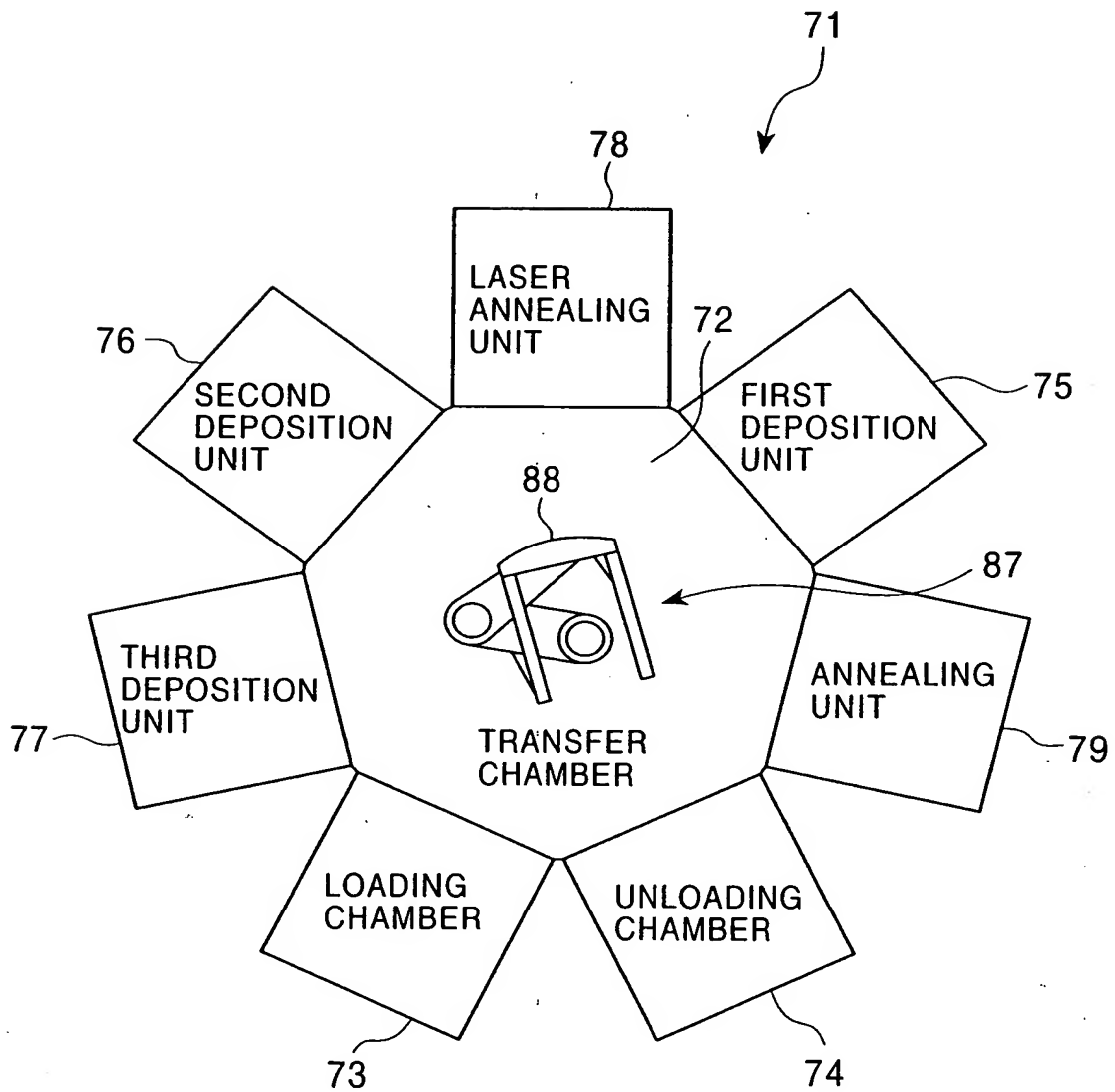
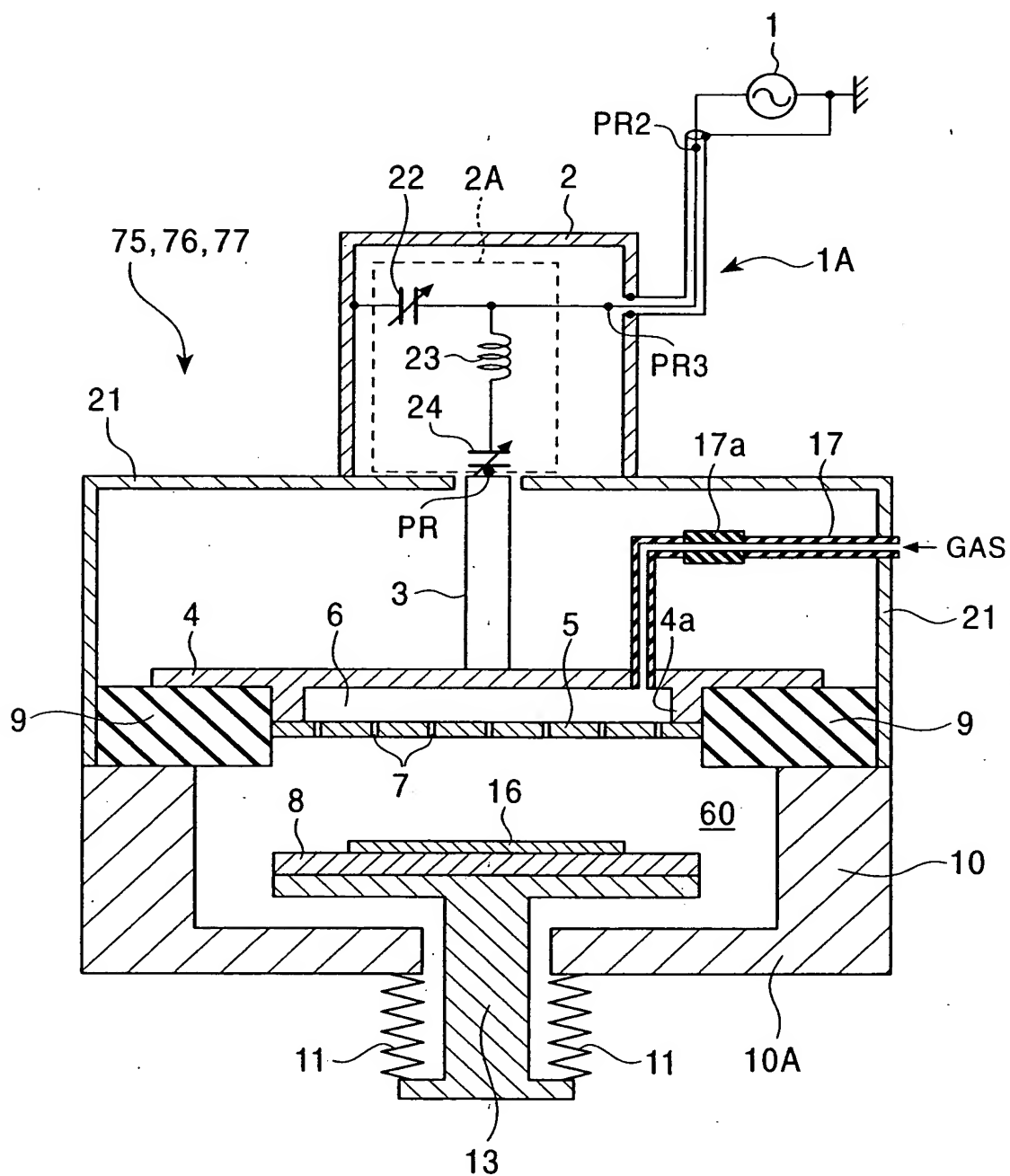


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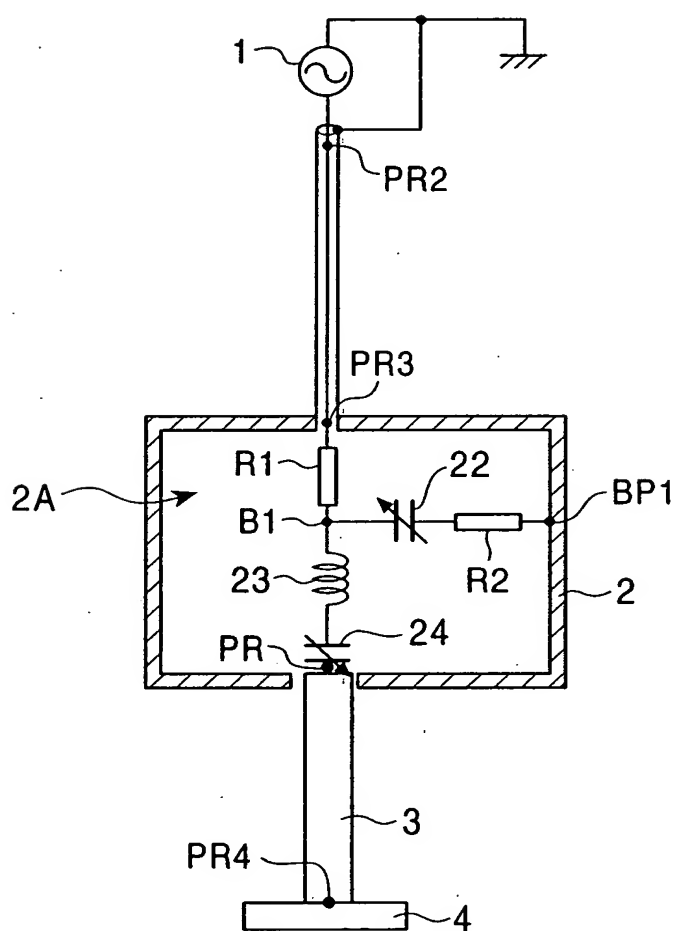
FIG. 1





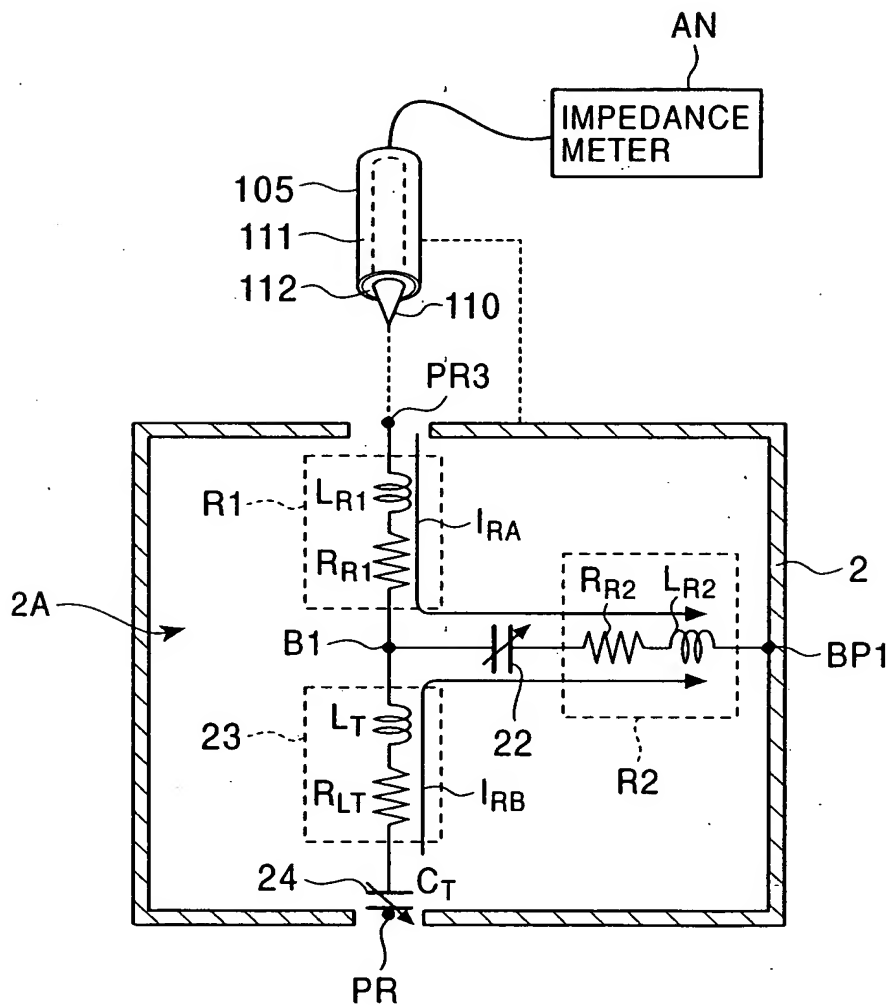
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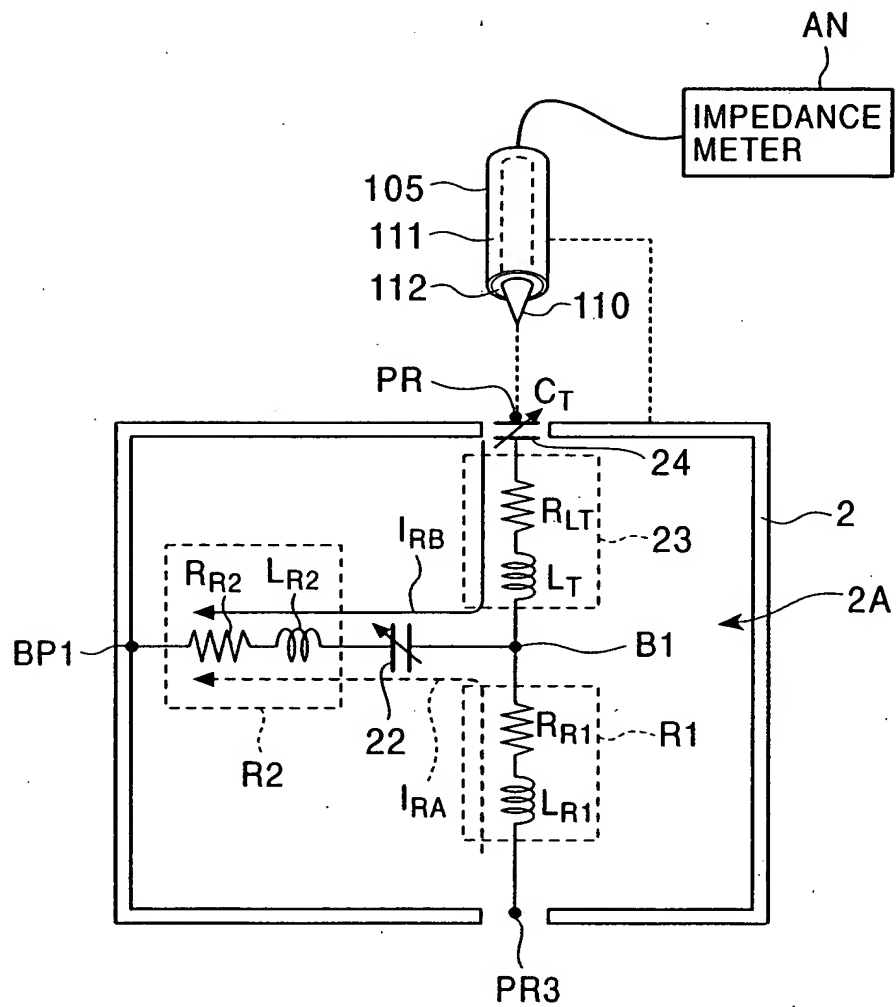
FIG. 3



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FIG. 4





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FIG. 6

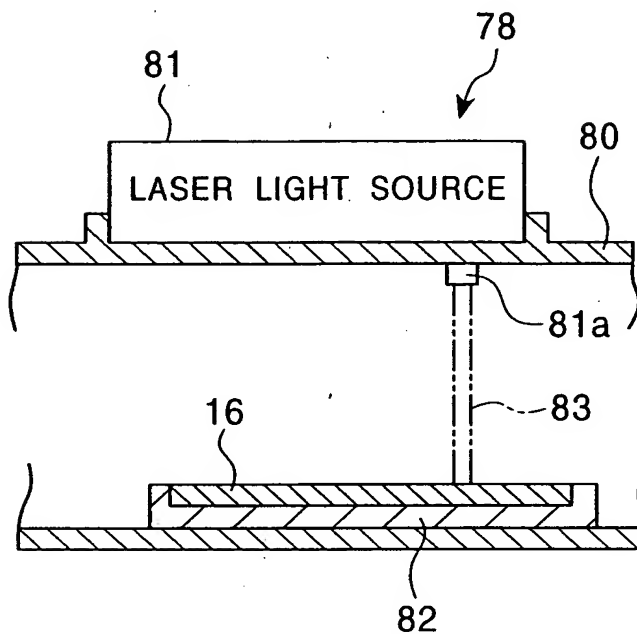
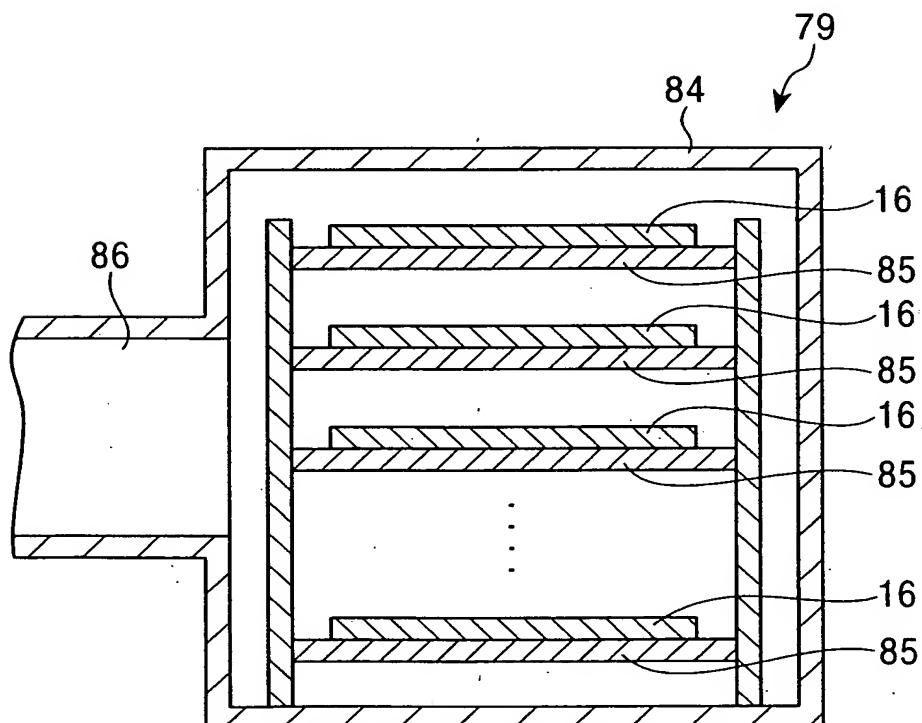


FIG. 7



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FIG. 8

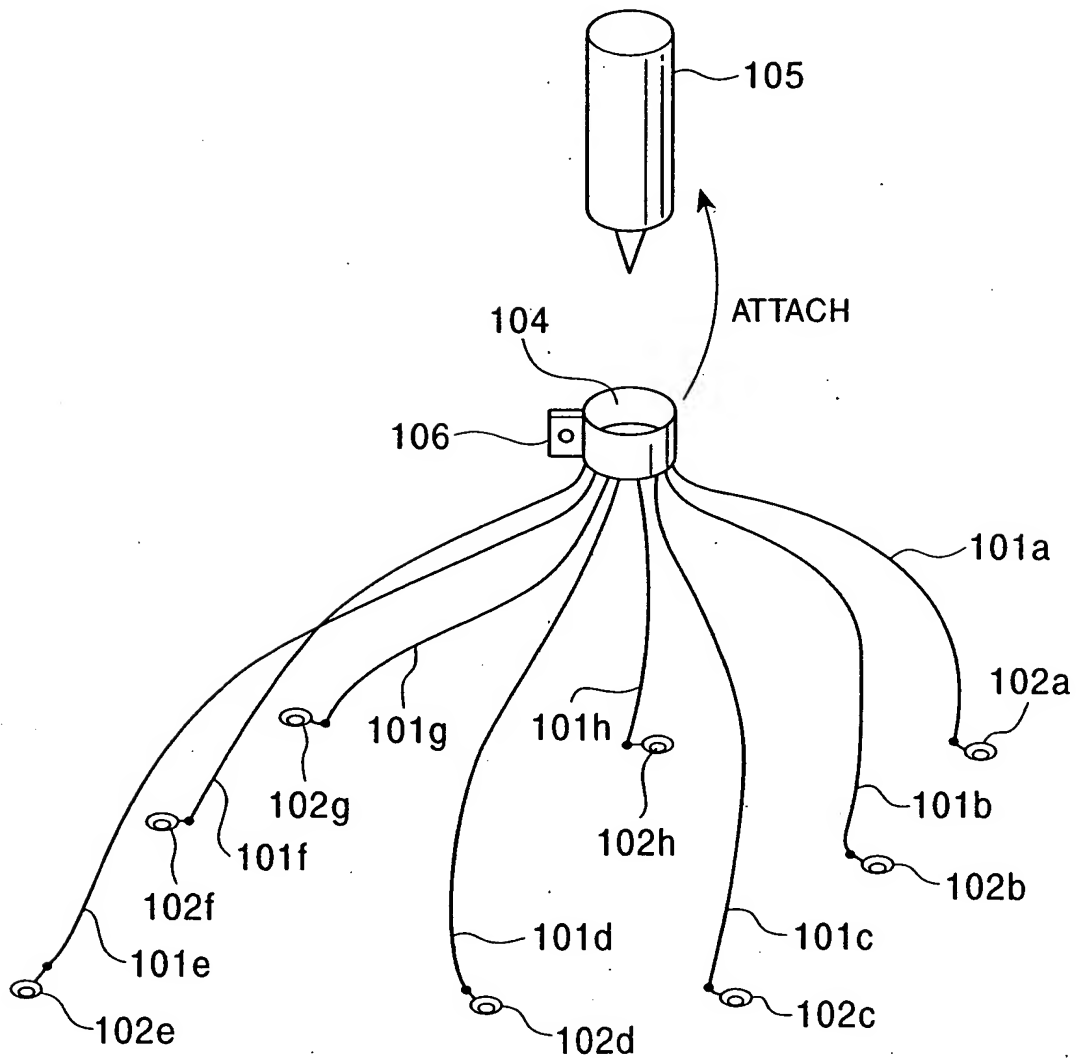
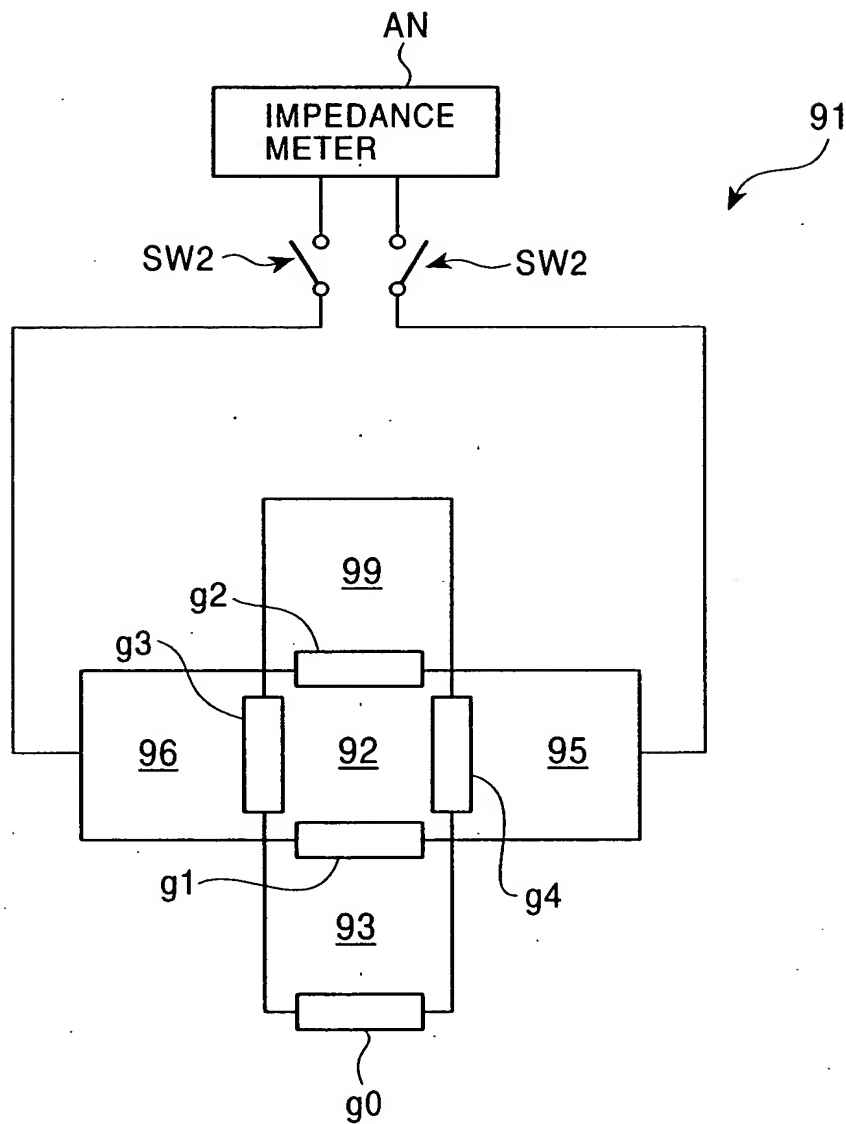


Figure 1 is a schematic diagram of a power supply circuit for a power semiconductor device. The device 105 is mounted on a substrate 2. It includes a base 110, a gate 101a, a drain 101b, and a source 101c. The source is connected to a common source terminal 102d. The drain is connected to a drain terminal 102a. The gate is connected to a gate terminal 102b. The circuit includes a resistor R1, an inductor LR1, a resistor RR1, a diode I_RA, a resistor R2, an inductor LR2, a resistor R_LT, an inductor LT, a capacitor CT, and a diode C_T. The circuit is connected to a power supply 2A and a load 2B.

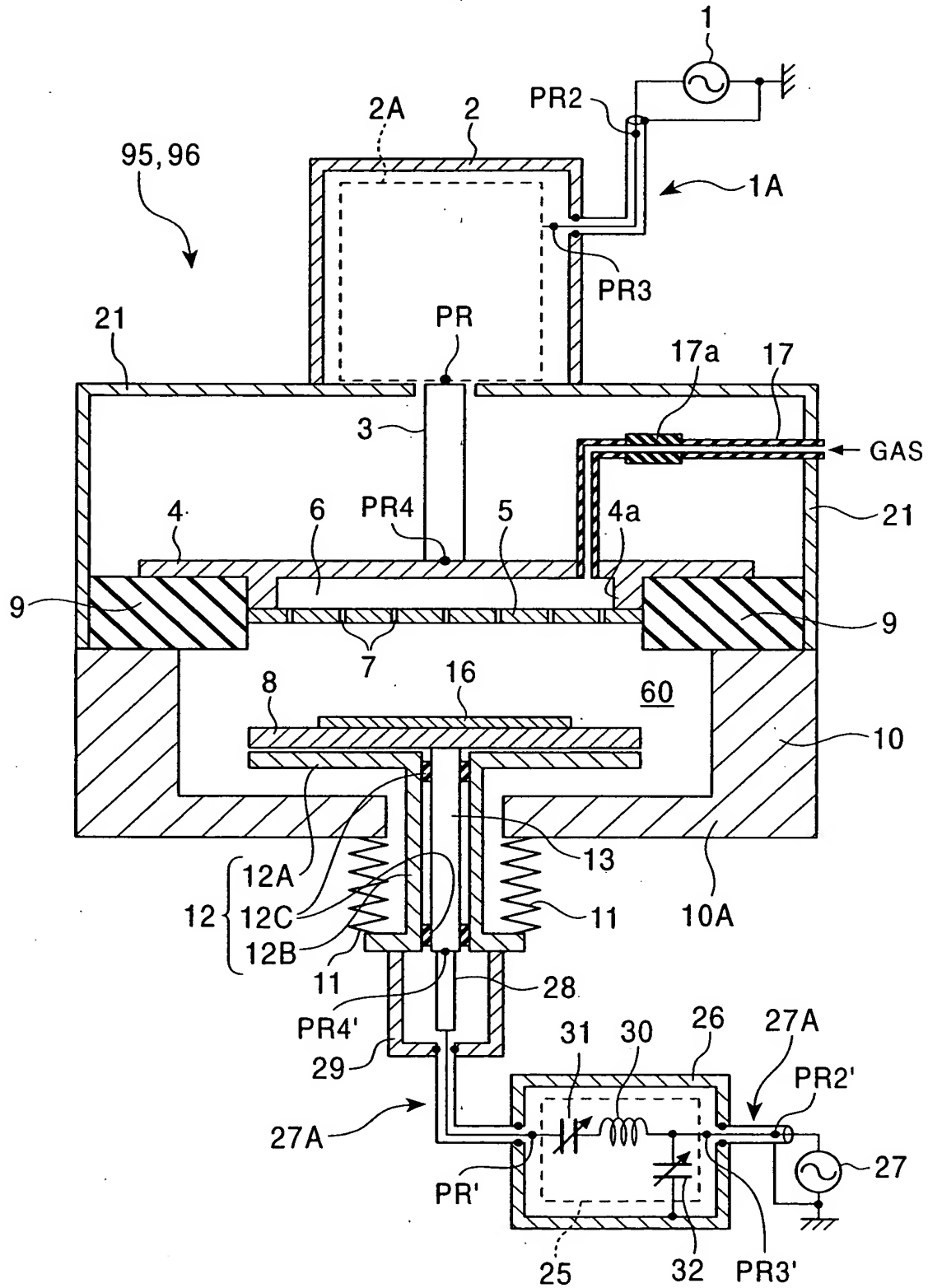
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FIG. 10



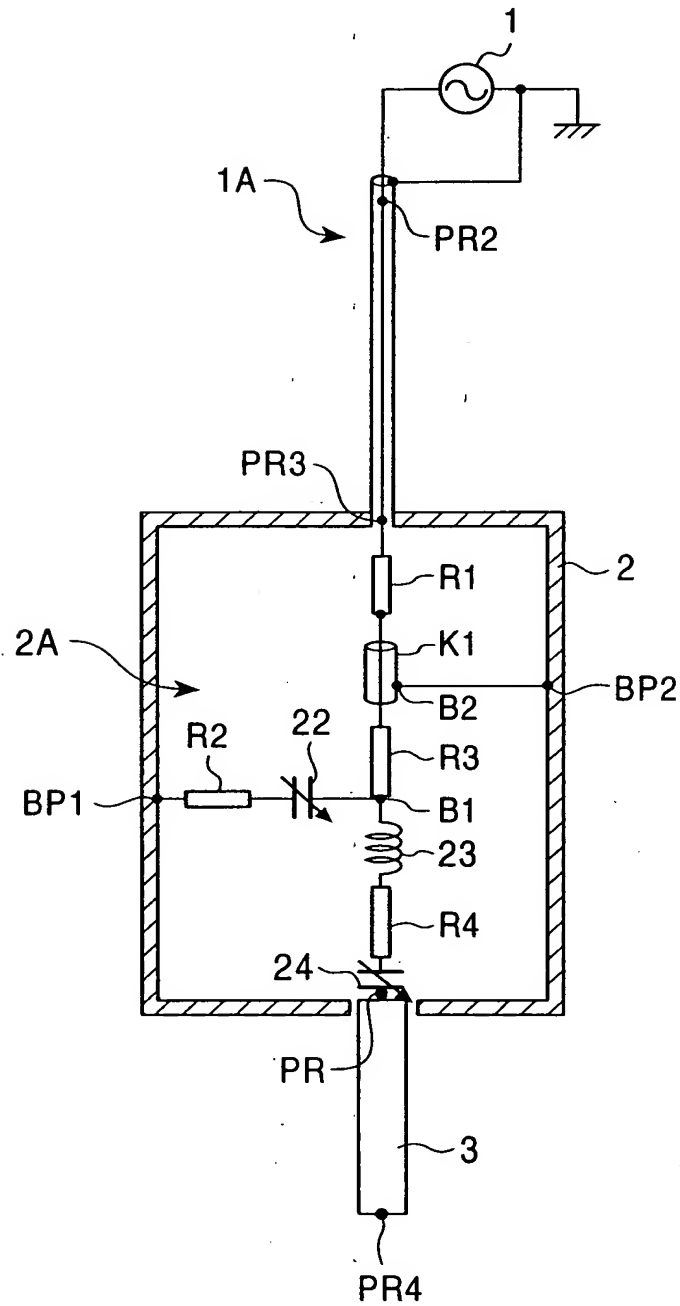
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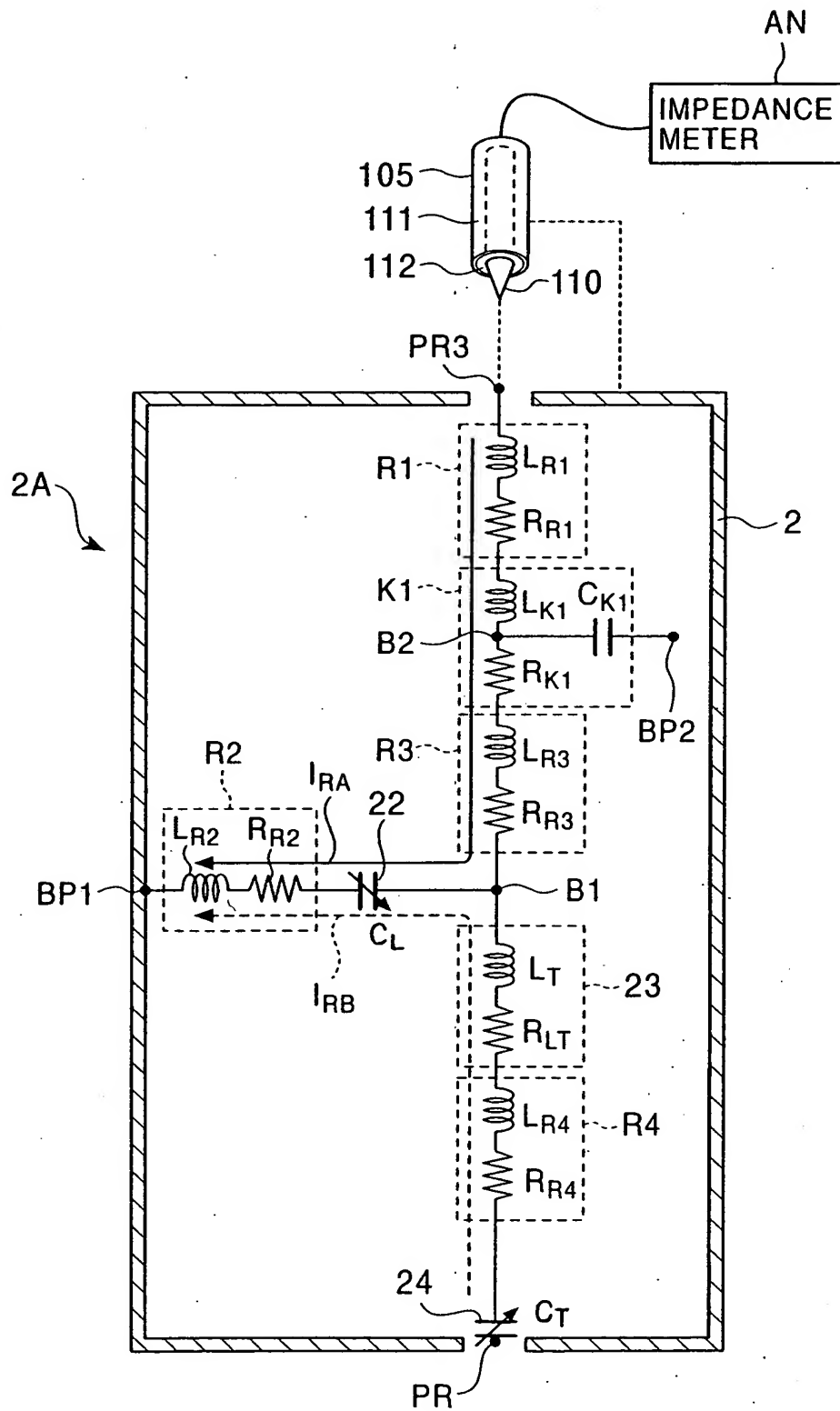
FIG. 11



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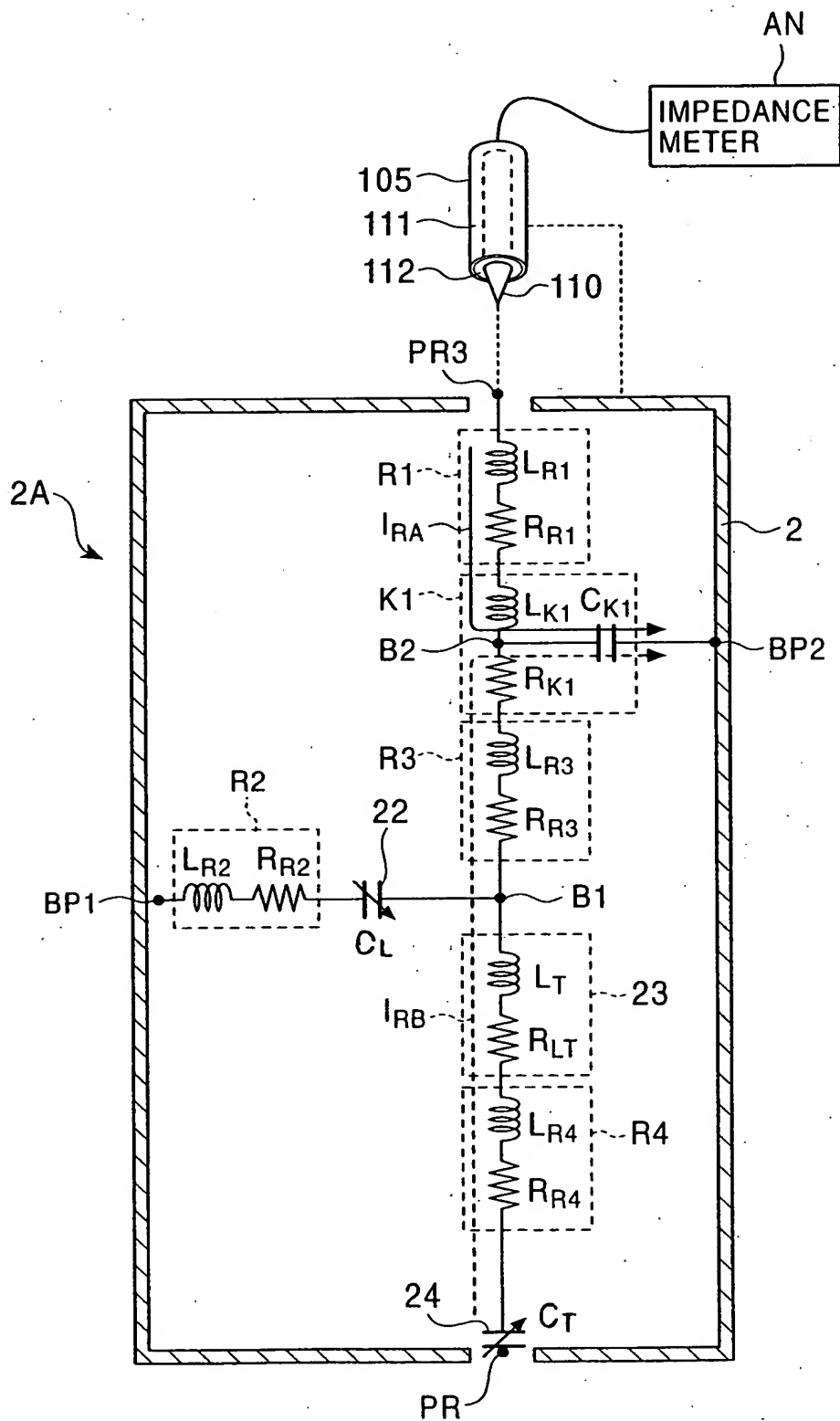
FIG. 12





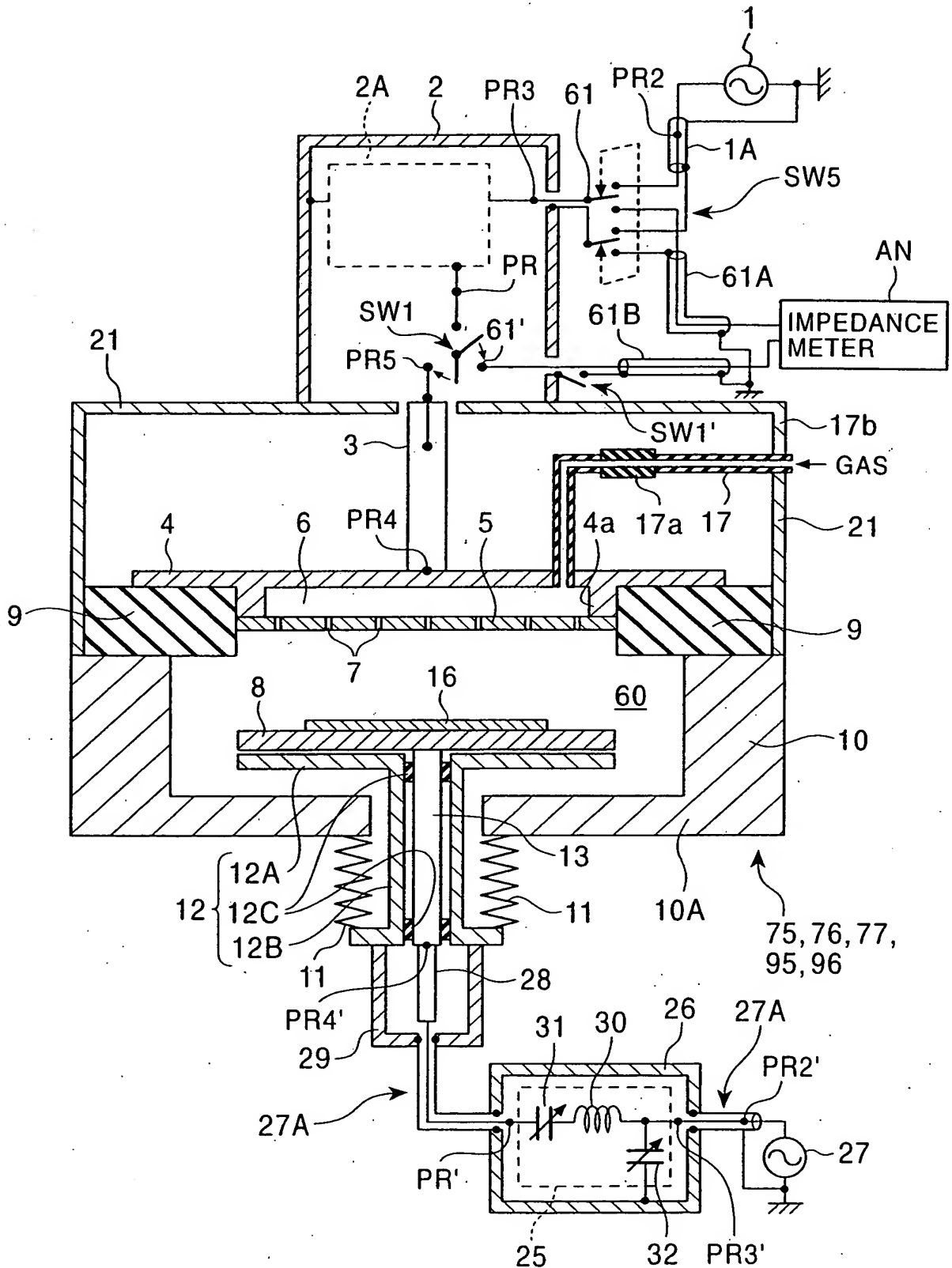
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FIG. 14



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FIG. 15



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FIG. 16

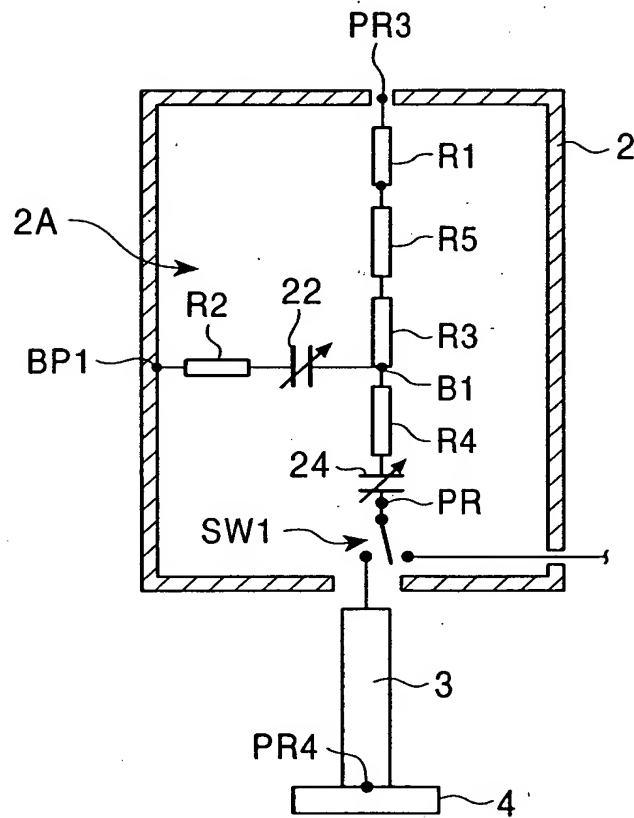
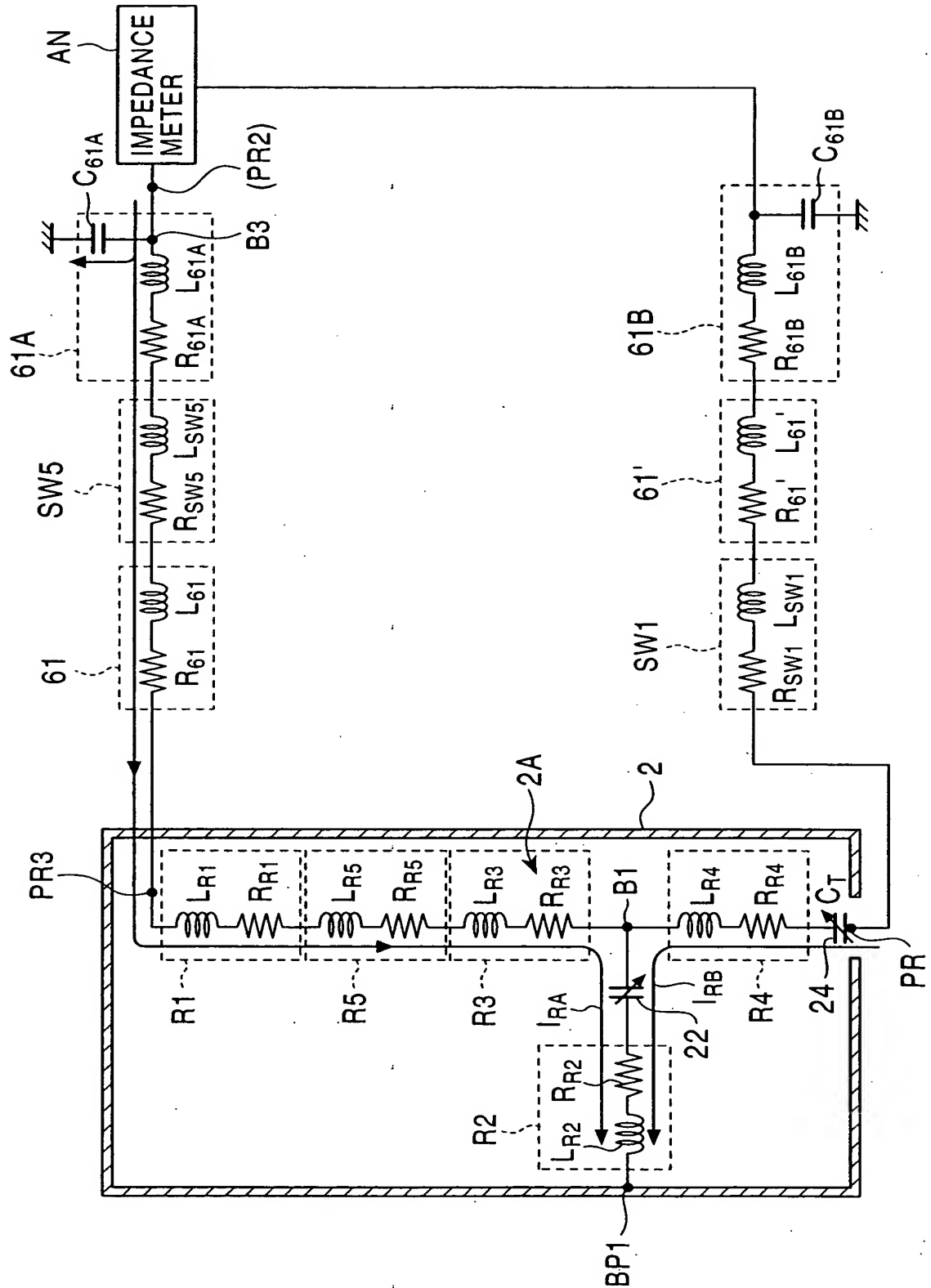
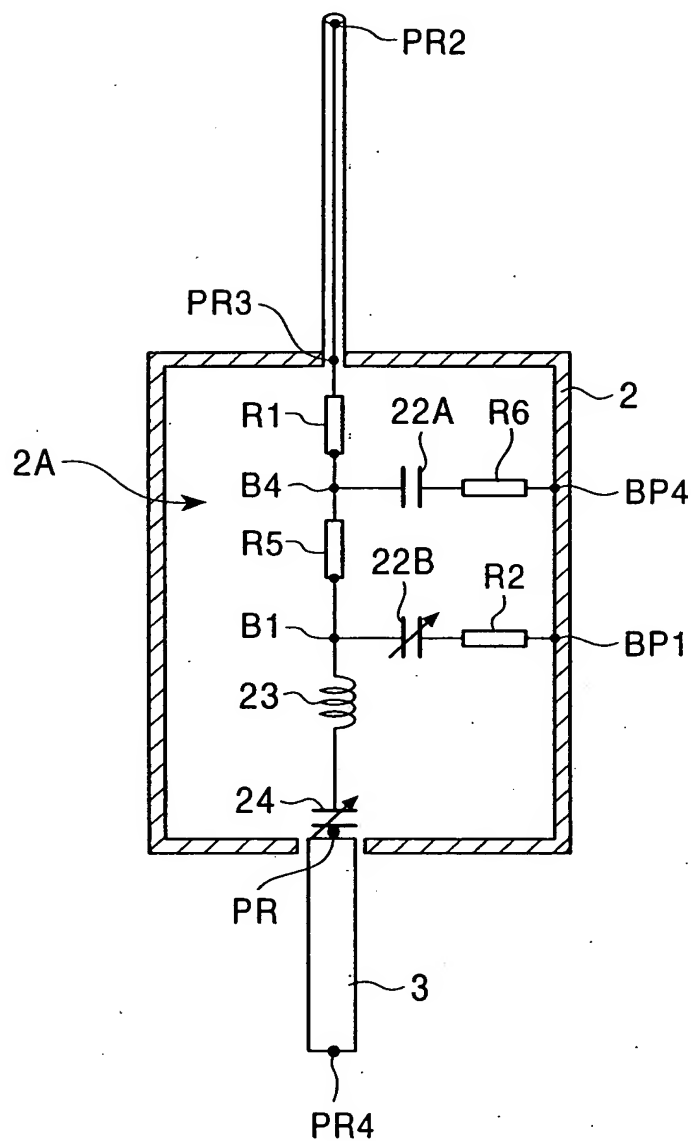


FIG. 17



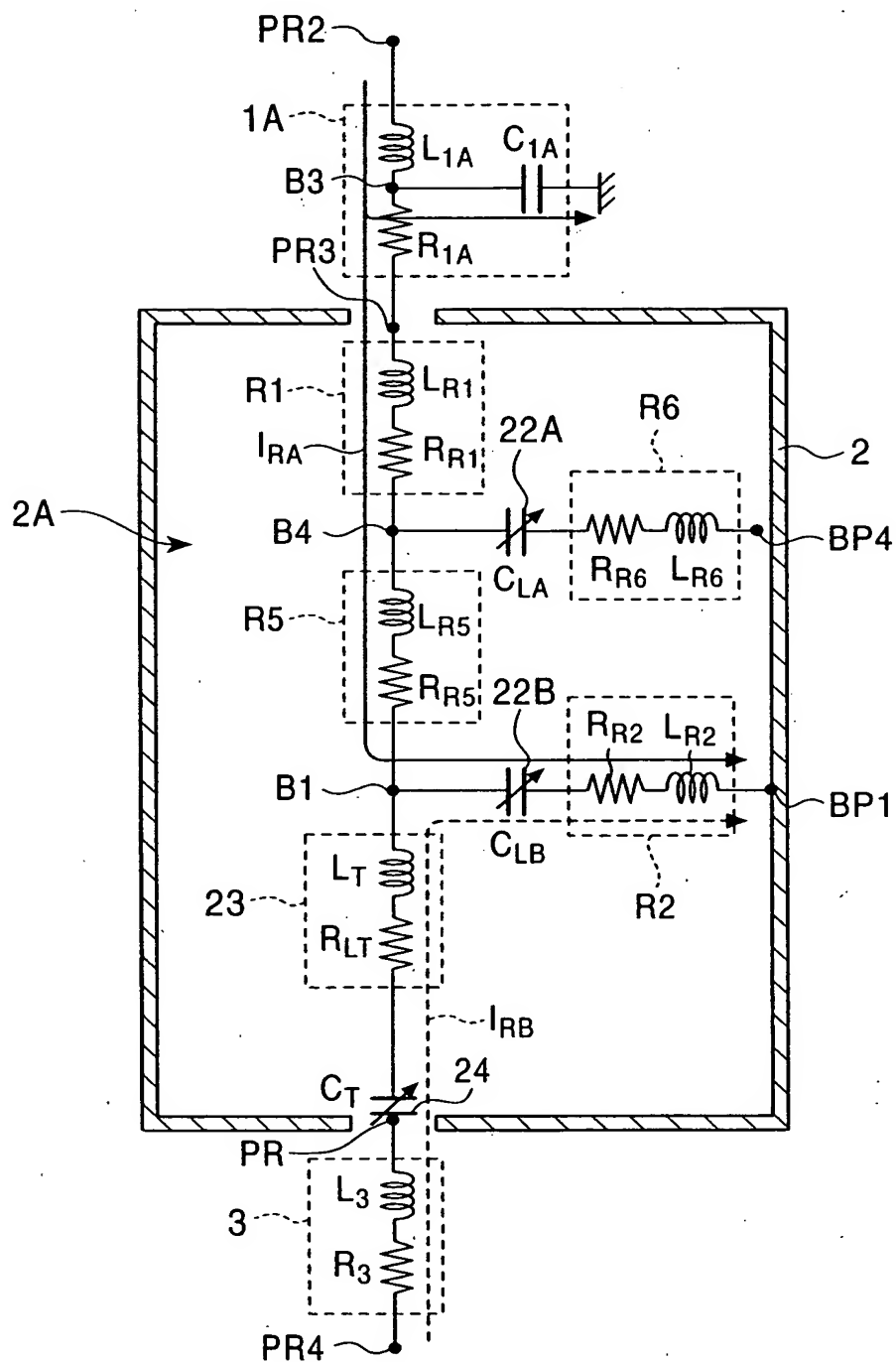
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FIG. 18



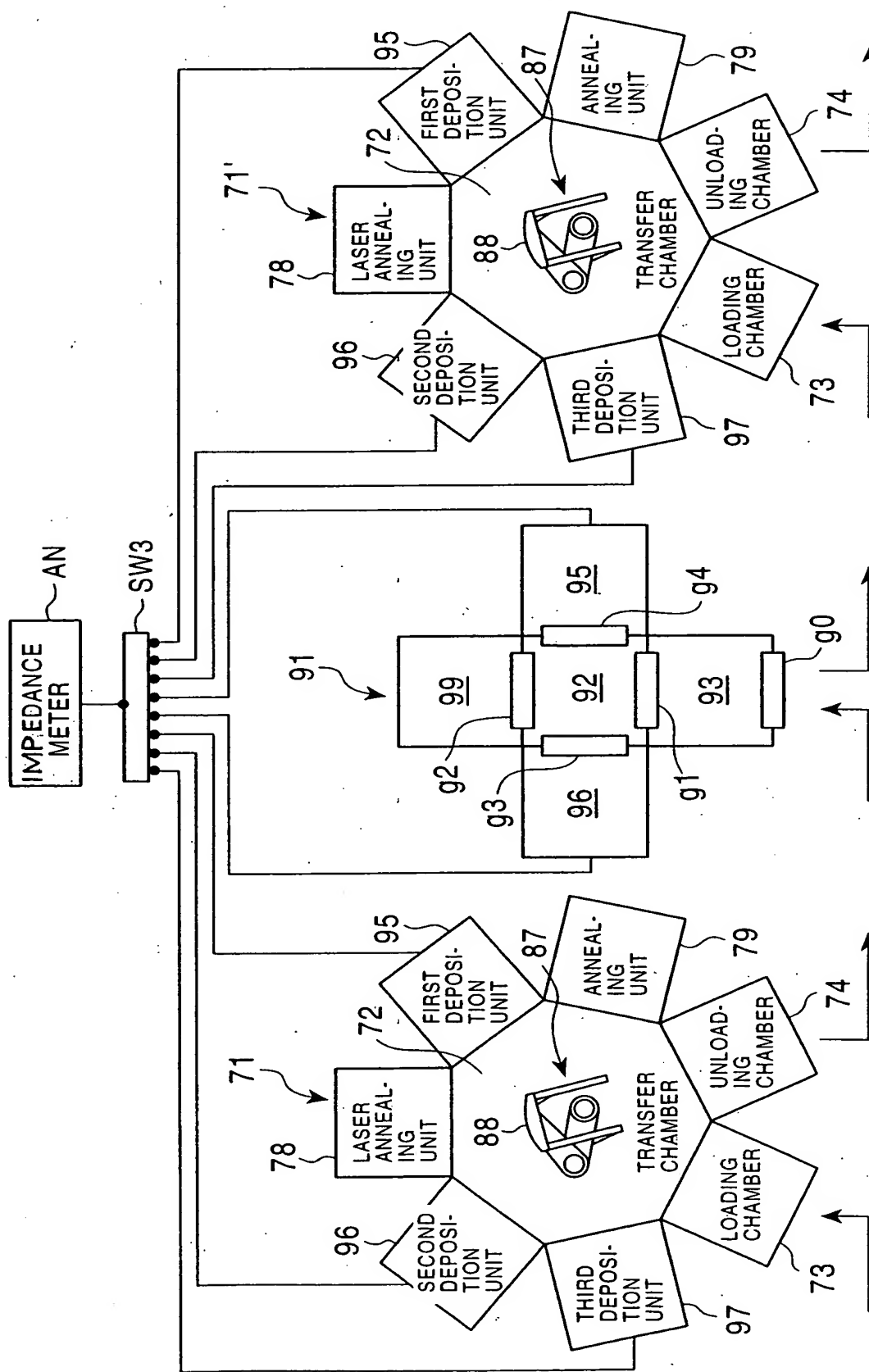
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FIG. 19



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FIG. 21



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FIG. 22

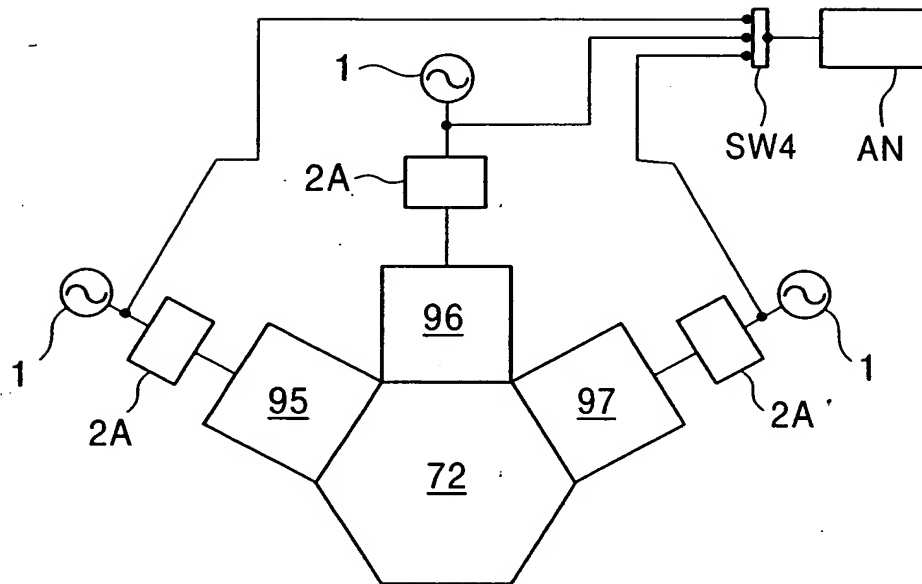
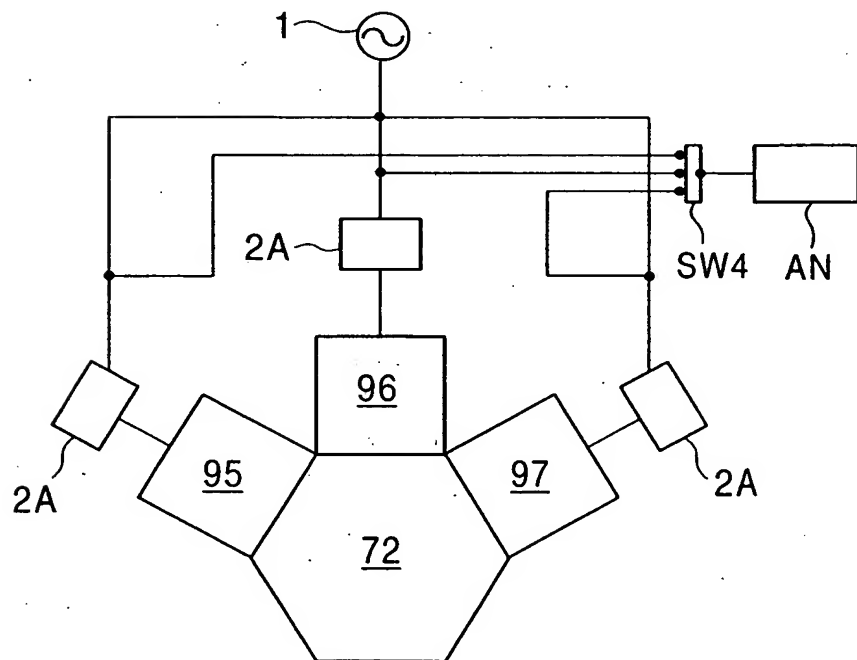


FIG. 23



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FIG. 24

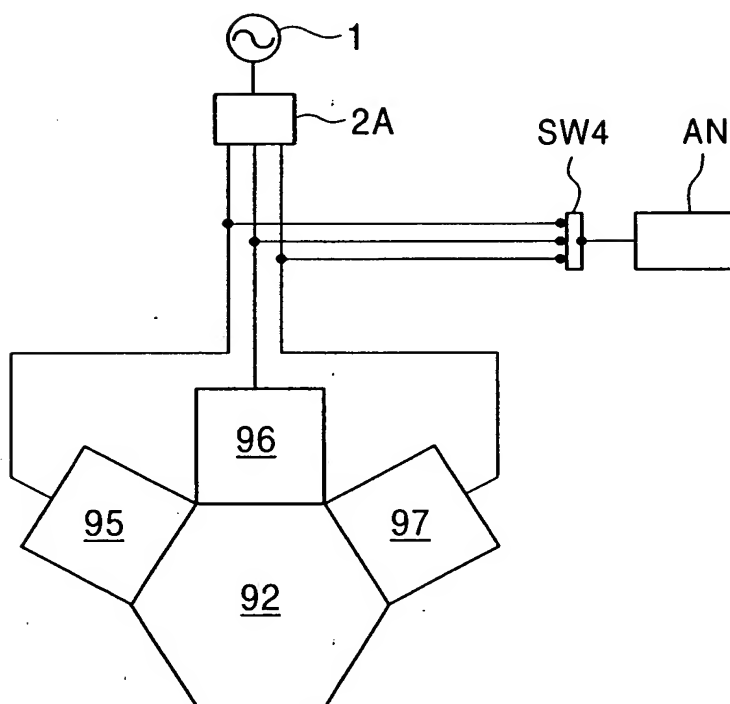
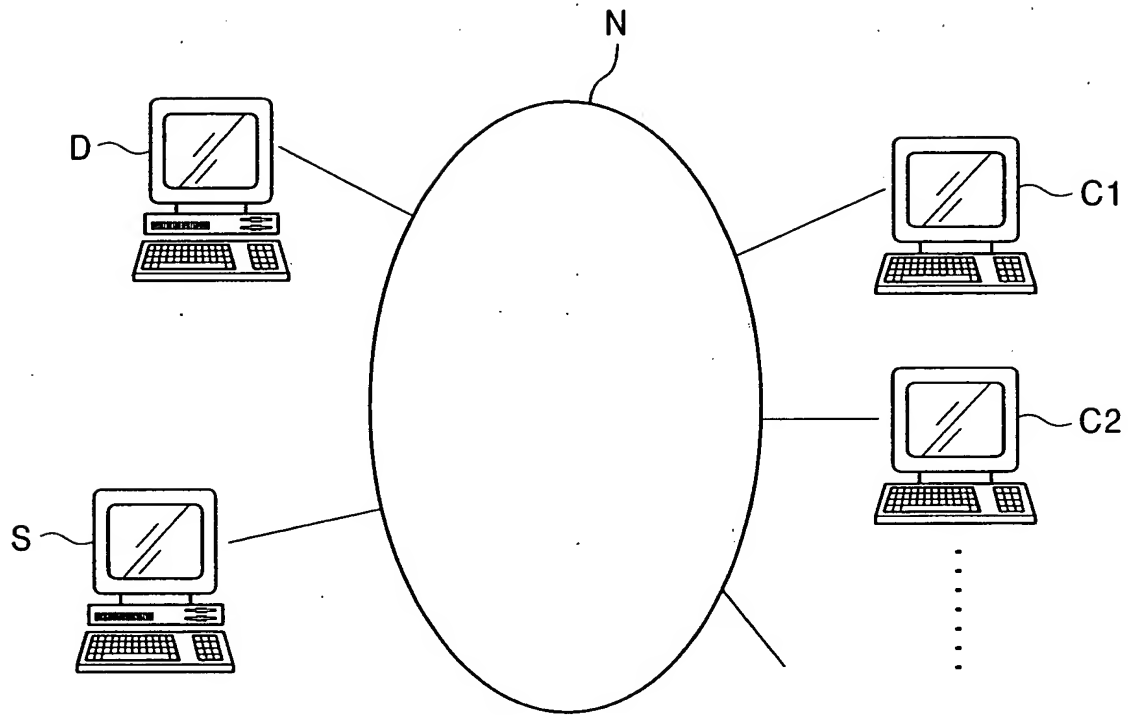
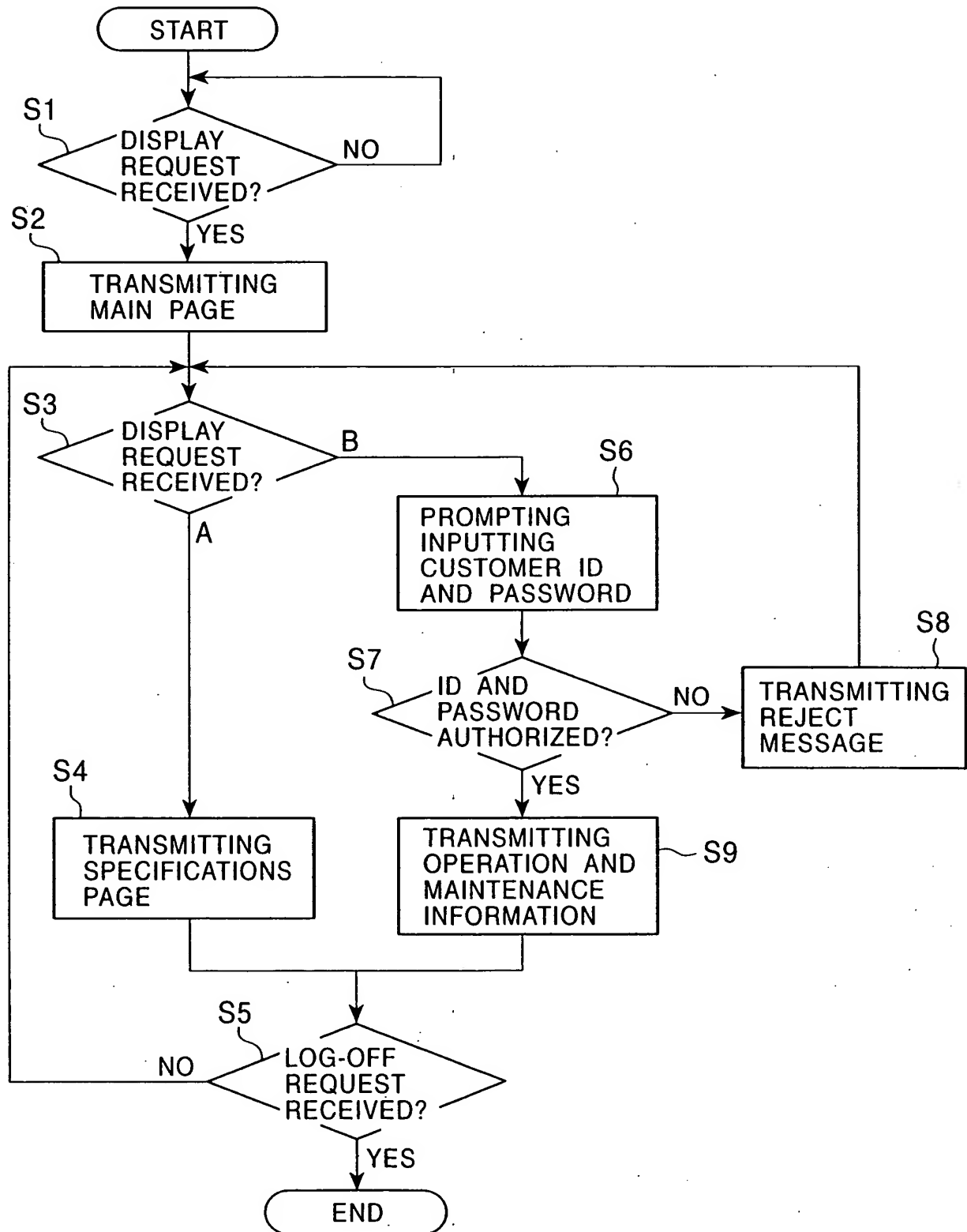


FIG. 25



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FIG. 26



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FIG. 27

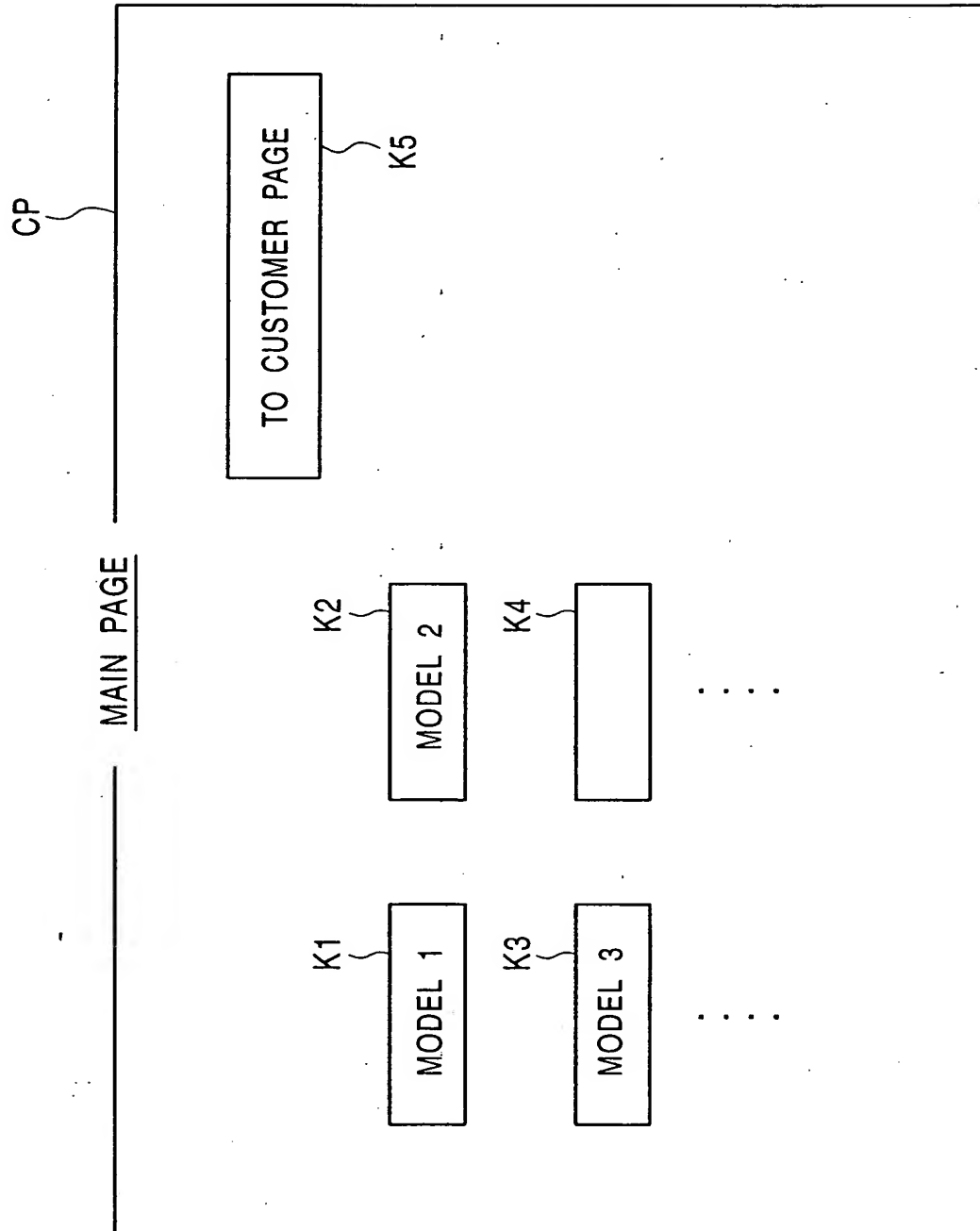


FIG. 28

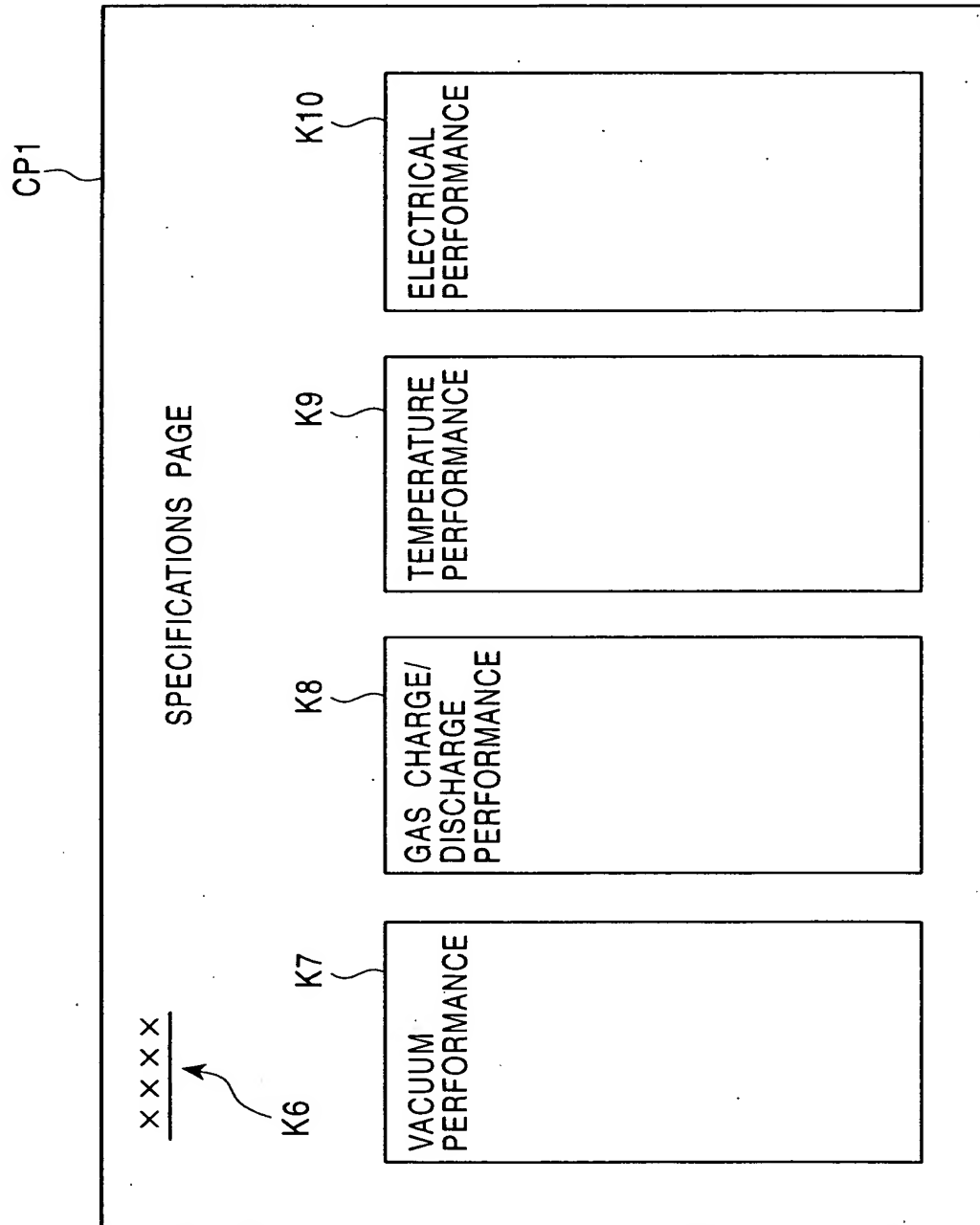
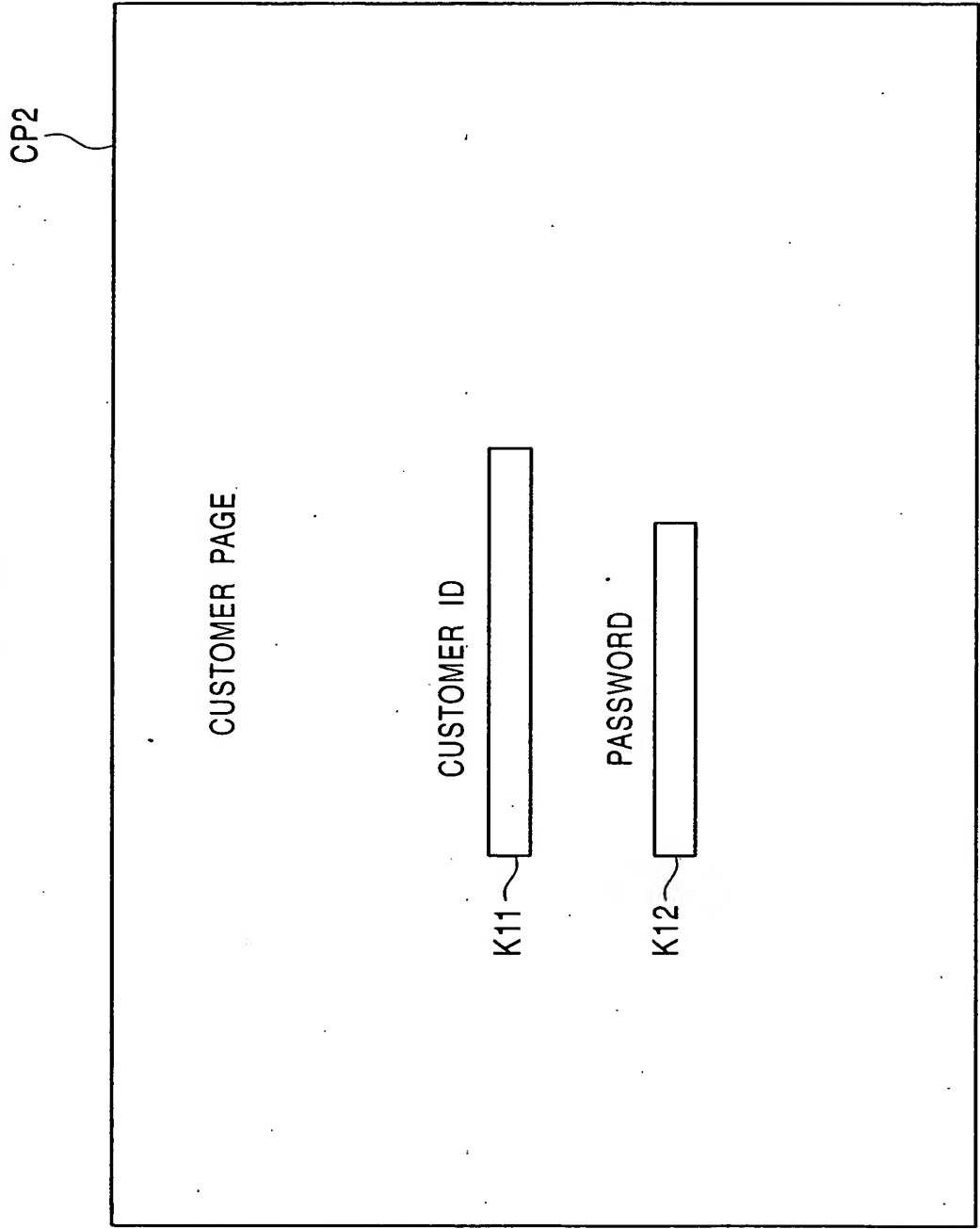


FIG. 29



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FIG. 30

CP3

MAINTENANCE HISTORY PAGE

K13

K7

VACUUM PERFORMANCE

K14

MAINTENANCE HISTORY

DE-
TAIL

DATE

K18

K17

K8

GAS CHARGE/
DISCHARGE
PERFORMANCE

K15

MAINTENANCE HISTORY

DE-
TAIL

DATE

K18

K9

TEMPERATURE
PERFORMANCE

K16

MAINTENANCE HISTORY

DE-
TAIL

DATE

K18

K10

ELECTRICAL
PERFORMANCE

MAINTENANCE HISTORY

DE-
TAIL

DATE

FIG. 31

CP4

DETAILED MAINTENANCE PAGE

XXXXX

K13

ELECTRICAL PERFORMANCE

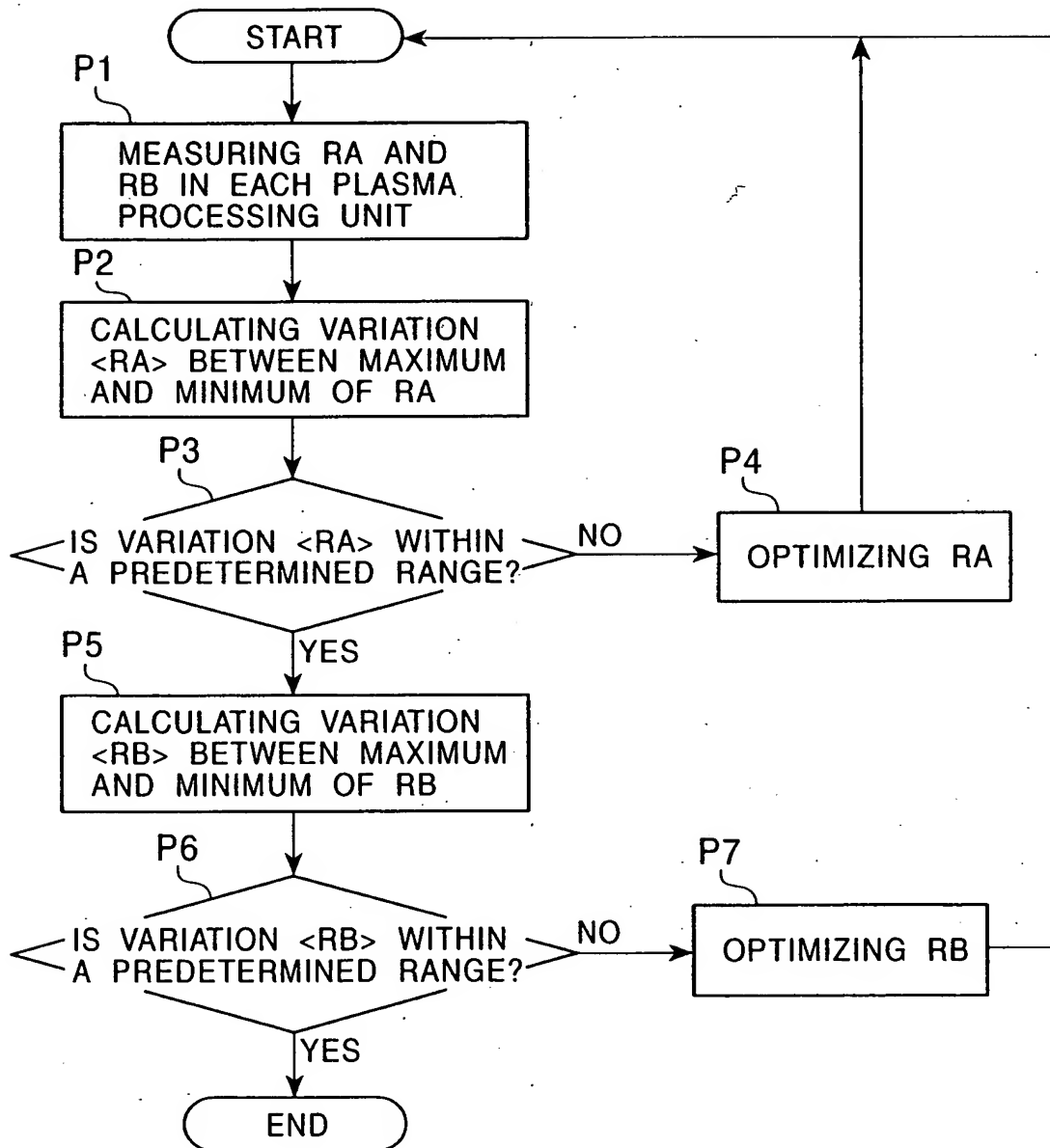
f _o ...	f _e ...	Z ...	RA ...	RB ...	C _e ...	C _x ...
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MAINTENANCE HISTORY

DATE	UNIT 1 XXXXX						UNIT 2 XXXXX						VARIATIONS XXXX							
	f _o	f _e	Z	RA	RB	C _x	f _o	f _e	Z	RA	RB	C _x	f _o	f _e	Z	RA	RB	C _e	C _x	
	MHz	MHz	Ω	Ω	Ω	pF	MHz	MHz	Ω	Ω	Ω	pF	MHz	MHz	Ω	Ω	Ω	pF	pF	
10/6	45.3	40.68	8	0.53	3.1	37	45.3	40.68	8	0.53	3.1	37	1800	0.04	0.05	0.4	0.02	0.3	0.09	0.02

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FIG. 32



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